

Appl. No. 10/542,136
Amdt. Dated March 27, 2006
Reply to Office Action of December 27, 2005

Amendments to the Specification

Please replace the title on page 1, lines 1 to 2 with the following:

~~AN ANALOG TO DIGITAL CONVERSION ARRANGEMENT, A METHOD FOR
ANALOG TO DIGITAL CONVERSION AND A SIGNAL PROCESSING SYSTEM,
IN WHICH THE CONVERSION ARRANGEMENT IS APPLIED~~ ANALOG-TO-
DIGITAL CONVERTER HAVING INTERLEAVED COARSE SECTIONS COUPLED
TO A SINGLE FINE SECTION

Please replace the paragraph beginning on page 2, line 9 with the following:

IDC-A1,AMD

Although various types of coarse and fine analog-to-digital converters can be used, for example flash analog-to-digital converters, in a particular embodiment these coarse and/or fine resolution analog-to-digital converters are formed by successive approximation analog-to-digital converters. It is possible to form the coarse resolution converters by flash converters and the fine resolution converter by a successive approximation converter; although flash converters ~~has~~ have the disadvantage that they need more circuit blocks, they have the advantage that more bits can ~~be~~ be determined at the same time in the same clock period, while in successive approximation converters more clock periods are necessary to determine the successive bits. Also it is possible to apply successive approximation converters as coarse resolution converters and a flash converter as the fine resolution converter.

~~Please replace the paragraph beginning on page 4, line 11 with the following:~~

IDC-A2,AMD,M

- B In a more ^{simple form} ~~simple form~~ the successive approximation analog-to-digital converter operates
- B without so called overranging. This means that the voltage range for the determination of a bit value is divided in only two separated regions and by means of a single comparator unit is determined in which region the sample voltage value lies, which region again is

NM
5/24/06